# ImageWriter

## *SPANSION S25F Family Device Support Detail & Setup*

**ImageWriter Support Hierarchy**

**Manufacturer:** Spansion **Library:** SPI-Serial_Memory.cab

**Device:** S25FL004A, S25FL008A, S25FL016A, S25FL032A, S25FL064A, S25FL010A, S25FL020A, S25FL040A, S25FL080A

## Objects for S25FL Family

| *Flow Object Name:* | *Program / Execute* | *Verify* | *Load* | *Object Size (Bytes)* | |
|---|---|---|---|---|---|
| FLASH | • | • | • | S25FL004A | 0x80000 |
| | | | | S25FL008A | 0x100000 |
| | | | | S25FL016A | 0x200000 |
| | | | | S25FL032A | 0x400000 |
| | | | | S25FL064A | 0x800000 |
| | | | | S25FL010A | 0x20000 |
| | | | | S25FL020A | 0x40000 |
| | | | | S25FL040A | 0x80000 |
| | | | | S25FL080A | 0x100000 |
| Status Register | • | • | • | 0x01 | |
| CHIP ERASE | • | | | 0x00 | |
| SECTOR ERASE | • | | | S25FL004A | 0x80000 |
| | | | | S25FL008A | 0x100000 |
| | | | | S25FL016A | 0x200000 |
| | | | | S25FL032A | 0x400000 |
| | | | | S25FL064A | 0x800000 |
| | | | | S25FL010A | 0x20000 |
| | | | | S25FL020A | 0x40000 |
| | | | | S25FL040A | 0x80000 |
| | | | | S25FL080A | 0x100000 |
| Read ID[2] | | | • | 0x3 | |
| p_SPI[1] | • | | • | 0x8 | |

[1] Default SPI clock frequency = 8 MHz

[2] RDID identification instruction

## Physical Layer Connector Setup for S25FL Family

| ImageWriter: | | Target: |
|---|---|---|
| ***Connector Pin #*** | ***Pin Name*** | ***Connections*** |
| 1 | GND | GND |
| 2 | GND | GND |
| 3 | TXD (MOSI/TDO) | SI |
| 4 | RXD (MISO/TDI) | SO |
| 5 | RTS (TMS) | NOCONN |
| 6 | CTS | NOCONN |
| 7 | XCLK | SCK |
| 8 | RST | !CS |
| 9 | SCL | NOCONN |
| 10 | SDA | NOCONN |
| 11 | GND | GND |
| 12 | GND | GND |
| 13 | VPP/EXT_IN | NOCONN |
| 14 | VPP/EXT_OUT | NOCONN |
| 15* | Target V+ | Supply 2.7 - 3.6V |
| 16* | Target V+ | Supply 2.7 - 3.6V |
| 17 | GND | GND |
| 18 | GND | GND |
| 19 | Pod Power | Supply 9-24 VDC |
| 20 | Pod Power | Supply 9-24 VDC |

## Support Notes:

1. A post-programming Flash Verify operation is highly recommended to ensure data integrity.
2. Erase & Program operations are dependent on Block Protect Bit settings in the Status Register. See manufacturer documentation for more details.
3. CHIP ERASE will bulk-erase entire device (See note 2.)
4. Sector ERASE will erase the addressed sector (See note 2.)
5. !W and !HOLD must be tied to VCC.

### Disclaimer

Typical implementation of **ImageWriter** includes but is not limited to external power provision to **ImageWriter**, device access restrictions on the target board, external hardware on the target board, or software changes created by the user affecting timing, device erasure, programming, or verification. Data I/O warranty coverage applies only when the product is used in conformance with technical specifications put forth by Data I/O and by the semiconductor device manufacturer.

In the interest of achieving the highest levels of safety and quality, Data I/O recommends that all programming processes be tested and documented in a prototype project prior to transition to volume manufacturing. For additional information see In-System Programming Design Guidelines (http://www.dataio.com/isp).

**ImageWriter** programs semiconductor devices through the use of circuits designed precisely to operate in accordance with specifications outlined by the device manufacturer. Provision and calibration of power supplies are critical for proper operation. **ImageWriter** has been fully tested by Data I/O Corporation for quality performance.